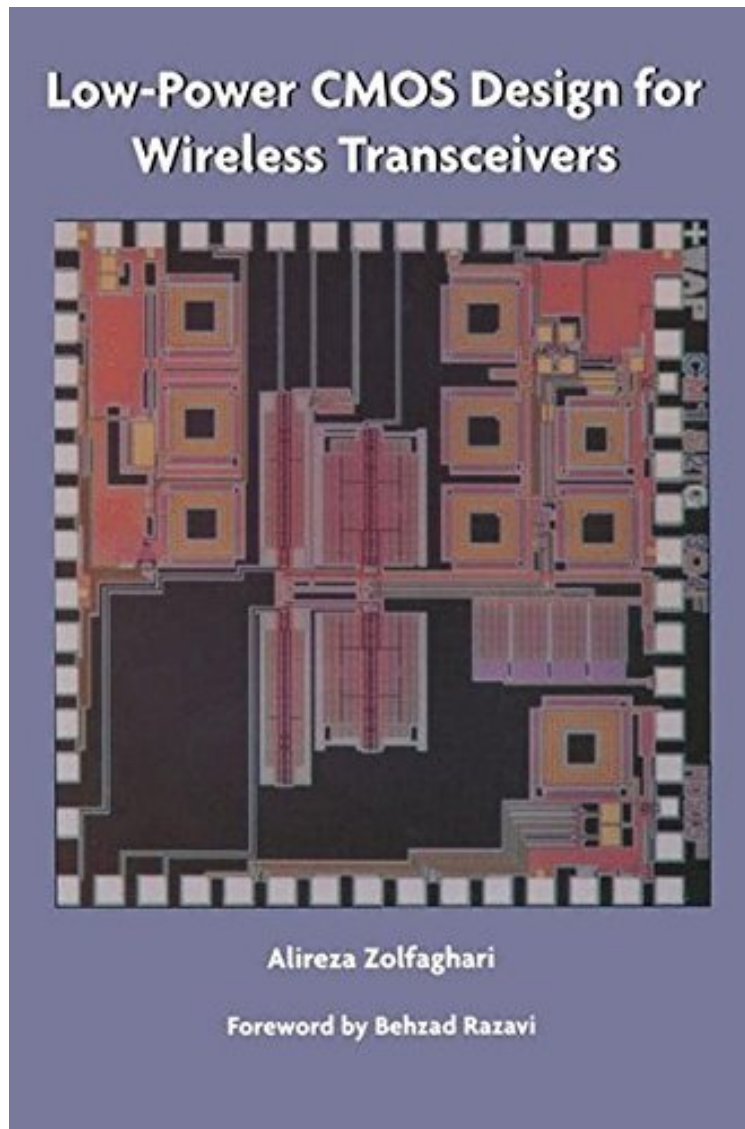


Low-Power CMOS Design for Wireless Transceivers

Alireza Zolfaghari

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2013-03-09 2013-03-09 File Name: B000UGN4G6 | File size: 74.Mb

Alireza Zolfaghari : Low-Power CMOS Design for Wireless Transceivers before purchasing it in order to gage whether or not it would be worth my time, and all praised Low-Power CMOS Design for Wireless Transceivers:

This comprehensive treatment of the challenges in low-power RF CMOS design deals with the design and implementation of low- power wireless transceivers in a standard digital CMOS process. It addresses trade-offs and

techniques that improve performance, from the component level to the architectural level.